

[First Hit](#)      [Previous Doc](#)      [Next Doc](#)      [Go to Doc#](#)

End of Result Set

☐ [Generate Collection](#) [Print](#)

L17: Entry 1 of 1

File: JPAB

Oct 23, 1990

PUB-NO: JP402260691A

DOCUMENT-IDENTIFIER: JP 02260691 A

TITLE: FORMATION OF CONDUCTIVE CIRCUIT

PUBN-DATE: October 23, 1990

## INVENTOR-INFORMATION:

NAME

COUNTRY

HIRAMA, YOSHINORI

ISHIBASHI, TATSUO

## ASSIGNEE-INFORMATION:

NAME

COUNTRY

NISSHA PRINTING CO LTD

APPL-NO: JP01083257

APPL-DATE: March 31, 1989

INT-CL (IPC): H05K 3/18

## ABSTRACT:

PURPOSE: To improve plating nucleuses in uniform dispersion after chemical reduction so as to enable the formation of a fine pattern by a method wherein an organic metal compound or an organic metal complex of a plating nucleus precursor is dispersed into a solution which contains prepolymer.

CONSTITUTION: An organic metal compound or an organic metal complex, a photocuring prepolymer solution, and an organic solvent are mixed to form a photosensitive ink, it is applied onto an insulating board, and a photosensitive ink layer is exposed to light for the formation of a circuit pattern. Not only the non-exposed part of the ink layer is removed by dissolution with a developing solution but also the surface of the exposed part of the ink layer is roughened at the same time. Then, the roughened ink layer is processed with a reluctant to turn the organic metal compound or the organic metal complex dispersed in the ink into plating nucleuses, and a metal layer is formed on the ink layer by dipping the board into electroless plating bath. By this setup, plating nucleuses are high in uniform dispersion, so that a fine pattern can be formed.

COPYRIGHT: (C)1990, JPO&amp;Japio

[Previous Doc](#)      [Next Doc](#)      [Go to Doc#](#)

[First Hit](#)[Previous Doc](#)[Next Doc](#)[Go to Doc#](#)

Generate Collection

Print

L26: Entry 4 of 15

File: JPAB

Sep 24, 1992

PUB-NO: JP404268070A

DOCUMENT-IDENTIFIER: JP 04268070 A

TITLE: FORMATION OF METALLIC LAYER ON SUBSTRATE

PUBN-DATE: September 24, 1992

## INVENTOR-INFORMATION:

NAME

COUNTRY

ESROM, HILMAR

## ASSIGNEE-INFORMATION:

NAME

COUNTRY

ABB PATENT GMBH

APPL-NO: JP03287763

APPL-DATE: November 1, 1991

INT-CL (IPC): C23C 14/28; C23C 14/14; C23C 14/58; C23C 18/31; C25D 5/54

## ABSTRACT:

PURPOSE: To provide an economical method for forming a metallic layer on a substrate without exposing the substrate to a solvent.

CONSTITUTION: In this method, An UV transmissive or light transmissive base (1) coated with an organic metallic compound (2) is arranged in front of a substrate (3) at prescribed intervals (A) in such a manner that the organic metallic compound confronts the substrate (3). The organic metallic compound is subjected to the operation of a pulsification photon beam with a prescribed wavelength, preferably, of 190 to 550 nm. In this way, a metallic layer (8) is formed on the substrate from the organic metallic compound. This metallic layer (8) can be reinforced by an electroless method or an electroplating method.

COPYRIGHT: (C)1992, JPO

[Previous Doc](#)[Next Doc](#)[Go to Doc#](#)

## WEST Search History

DATE: Thursday, March 30, 2006

Hide?	Set Name	Query	Hit Count
	<i>DB=PGPB,USPT,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i>		
<input type="checkbox"/>	L26	L25 and l23	15
<input type="checkbox"/>	L25	l24 or solv\$5	7653591
<input type="checkbox"/>	L24	solut\$5 or liquid	6882567
<input type="checkbox"/>	L23	L22 and l20	22
<input type="checkbox"/>	L22	L21 or organ\$5	1816655
<input type="checkbox"/>	L21	metalorgan\$5 or organmeta\$5	4200
<input type="checkbox"/>	L20	L19 and l18	47
<input type="checkbox"/>	L19	esrom.inv.	67
<input type="checkbox"/>	L18	uv or vuv	298747
<input type="checkbox"/>	L17	L16 and l10	1
<input type="checkbox"/>	L16	electroless or currentless	45225
<input type="checkbox"/>	L15	l11 and l12	0
<input type="checkbox"/>	L14	L13 and l11	0
<input type="checkbox"/>	L13	L12 and electroless	2812
<input type="checkbox"/>	L12	circuit adj pattern	48846
<input type="checkbox"/>	L11	L10 and l6	119
<input type="checkbox"/>	L10	hirama.inv.	1542
<input type="checkbox"/>	L9	L8 and l5	3
<input type="checkbox"/>	L8	electrode or ohm\$5 or contact	4811886
<input type="checkbox"/>	L7	L6 and l5	1
<input type="checkbox"/>	L6	GATE OR DRAIN OR PIXEL	1564901
<input type="checkbox"/>	L5	6524663	4
<input type="checkbox"/>	L4	L3 and l2 and l1	2
<input type="checkbox"/>	L3	yokota.inv.	20015
<input type="checkbox"/>	L2	sumiya.inv.	6448
<input type="checkbox"/>	L1	hiyama.inv.	3282

END OF SEARCH HISTORY